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FIG. 1A

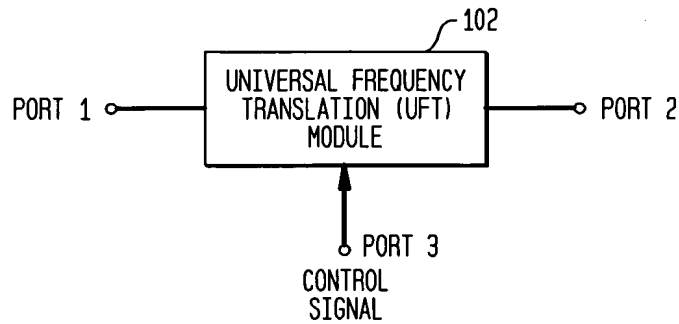
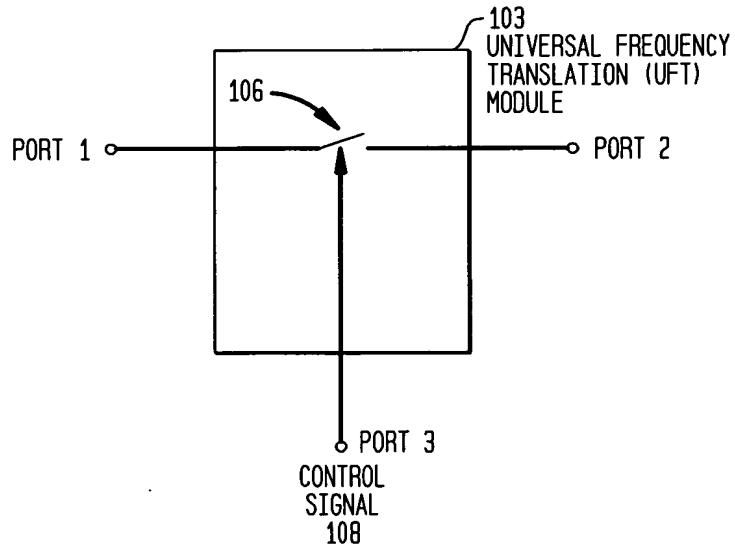


FIG. 1B



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FIG. 1C

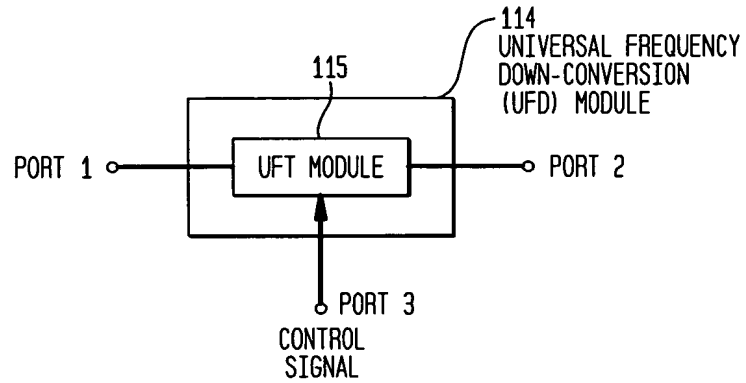
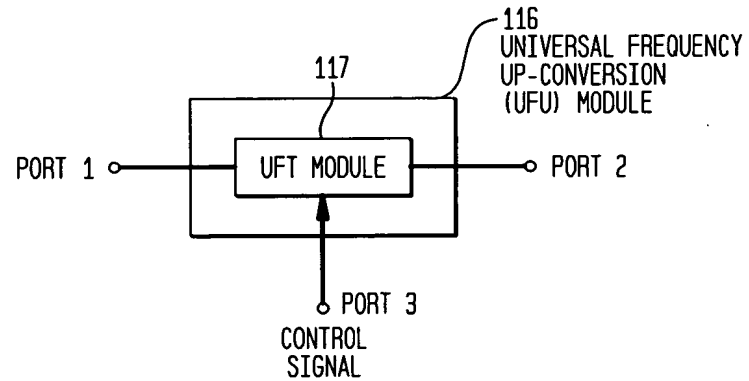
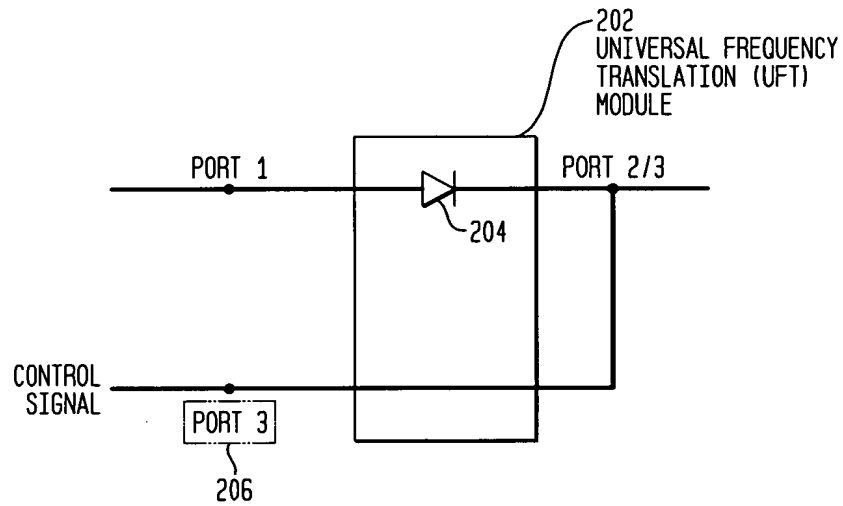


FIG. 1D



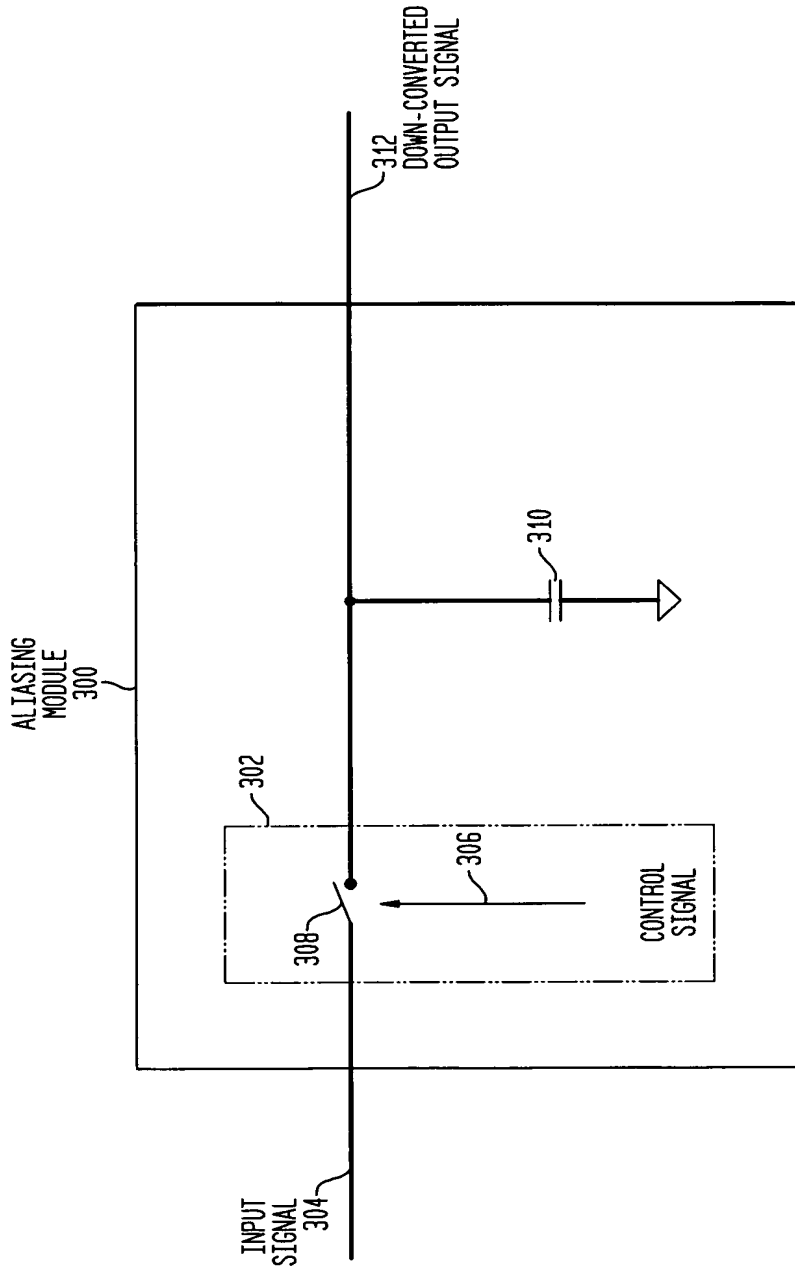
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FIG. 2



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FIG. 3A



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FIG. 3B

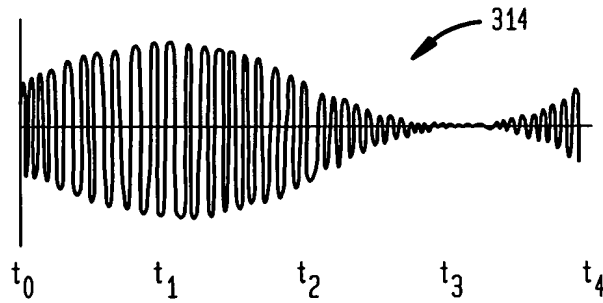


FIG. 3C

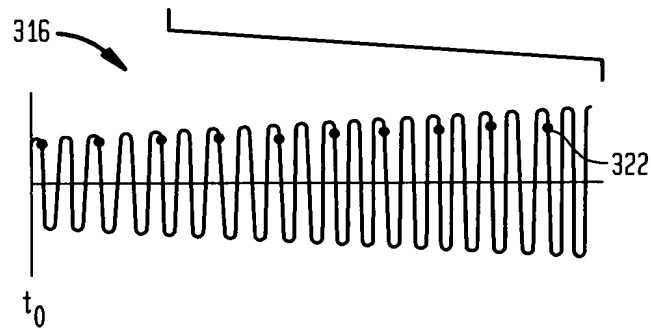


FIG. 3D

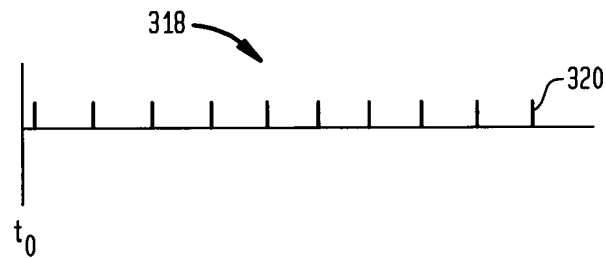


FIG. 3E

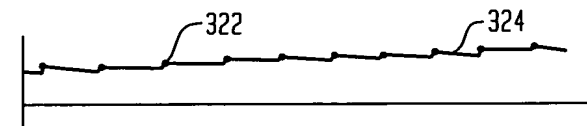
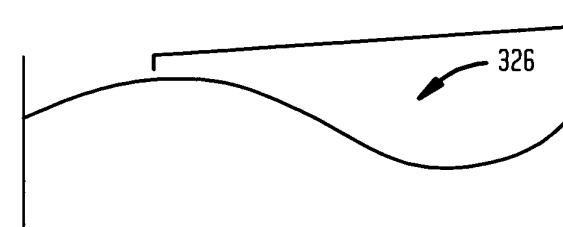
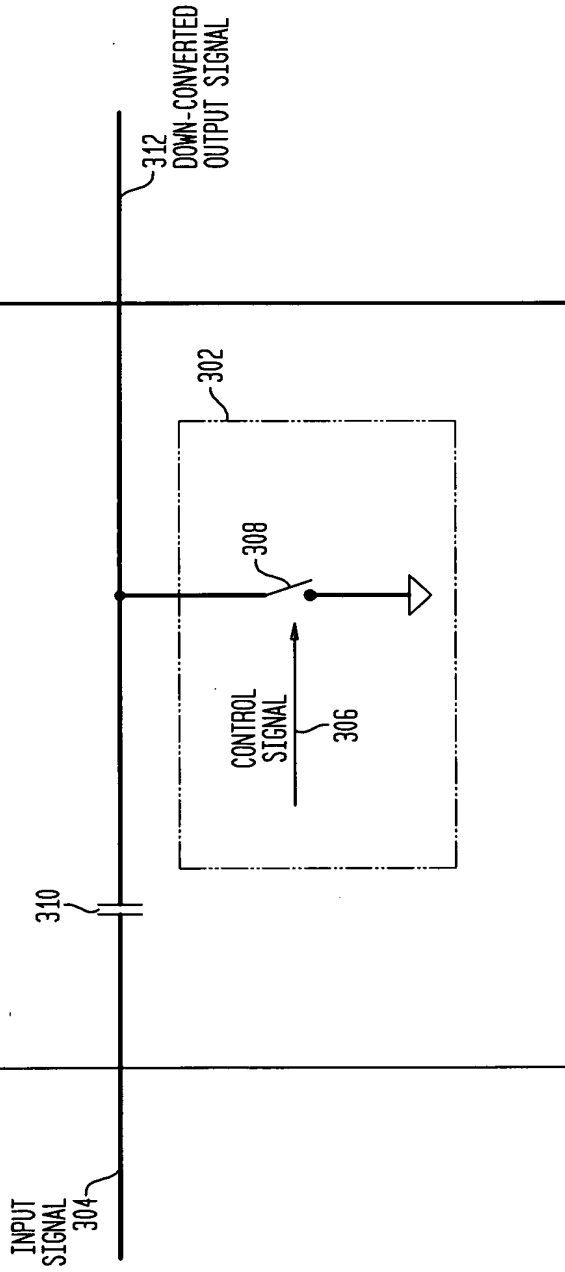


FIG. 3F



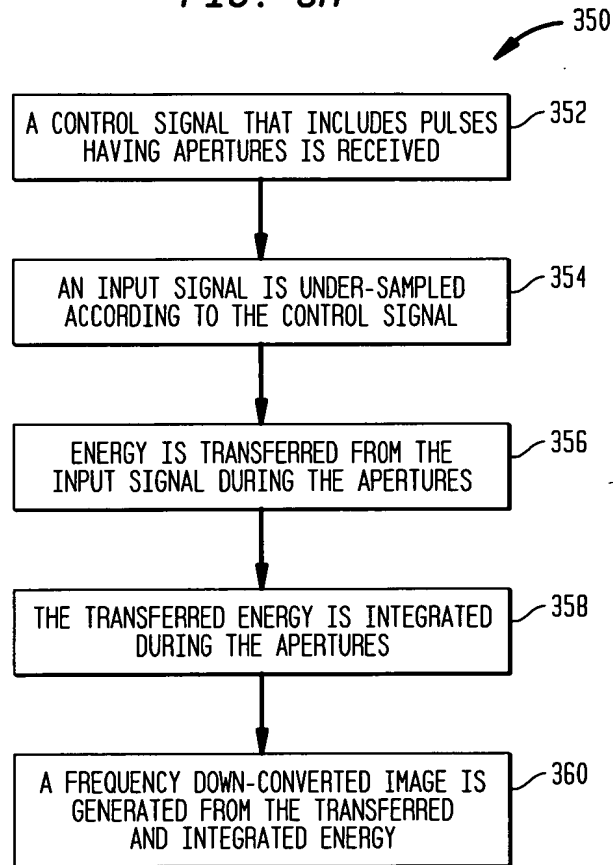
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FIG. 3G



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FIG. 3H



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FIG. 4

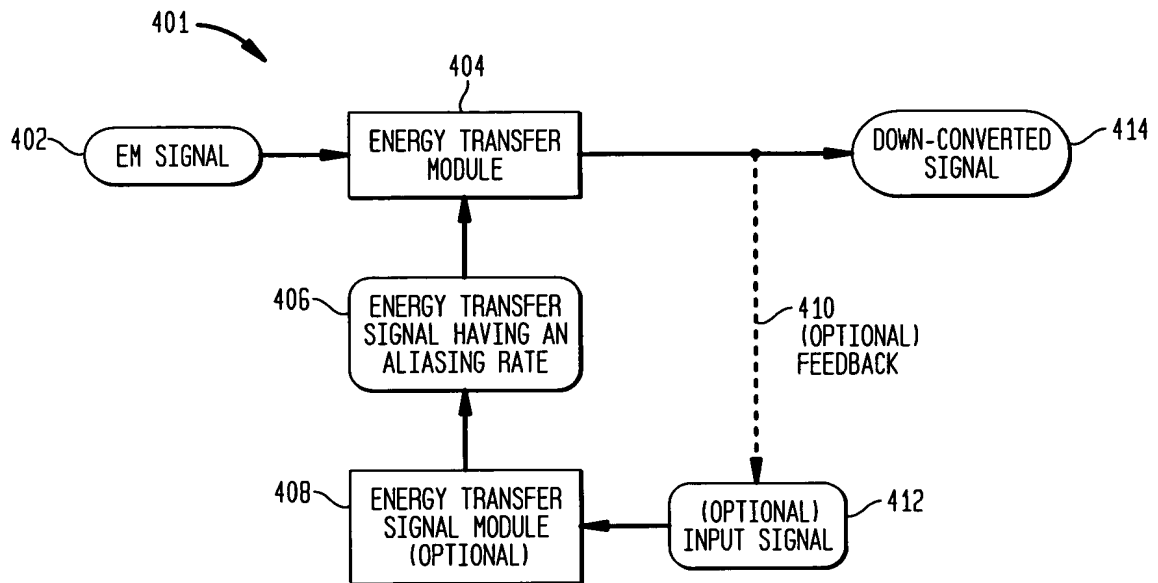
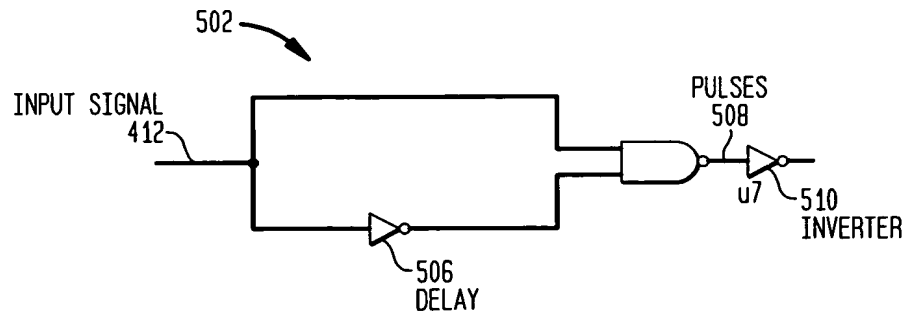
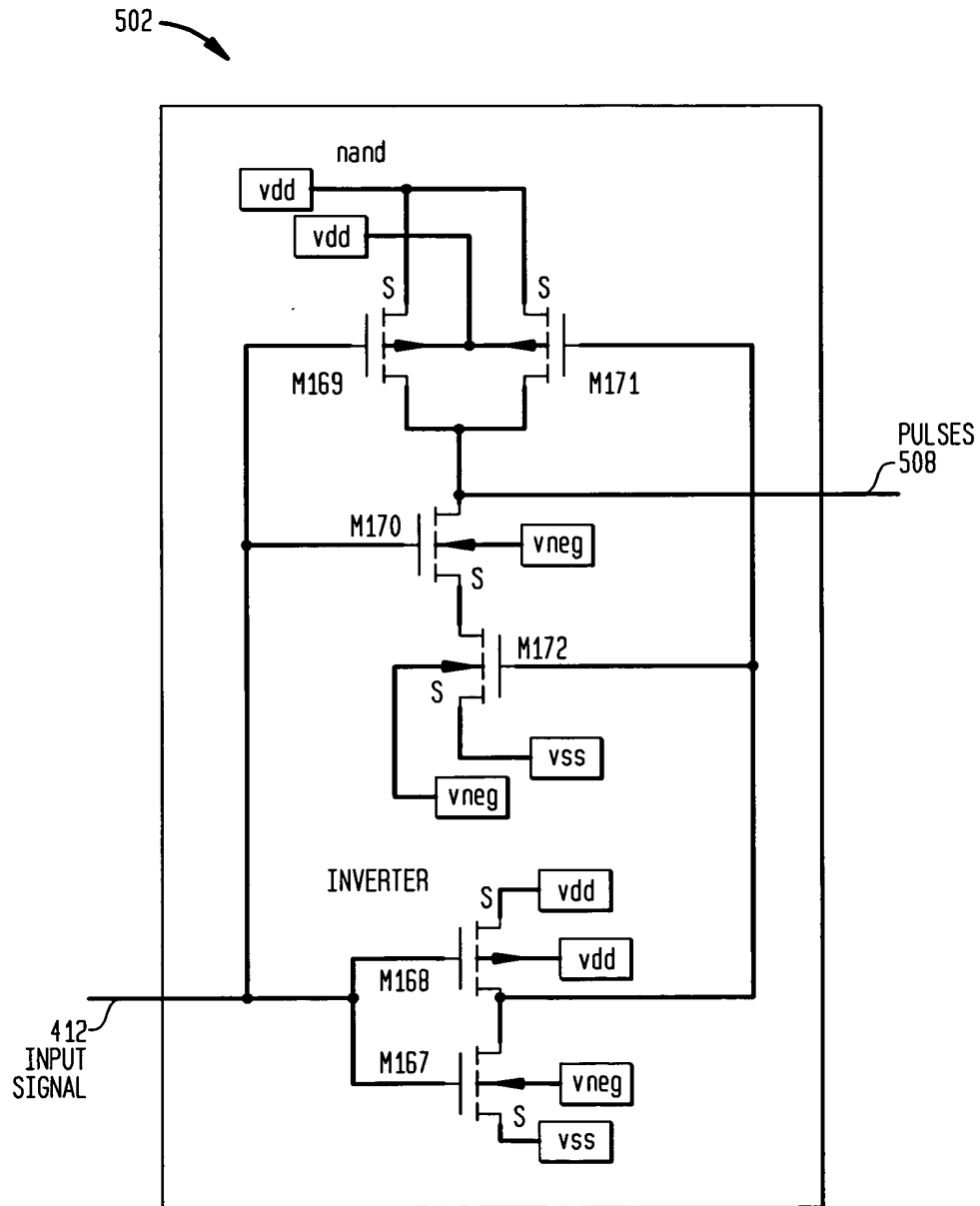


FIG. 5





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FIG. 6B

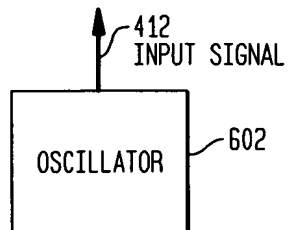


FIG. 7A
RISING EDGE PULSE GENERATOR

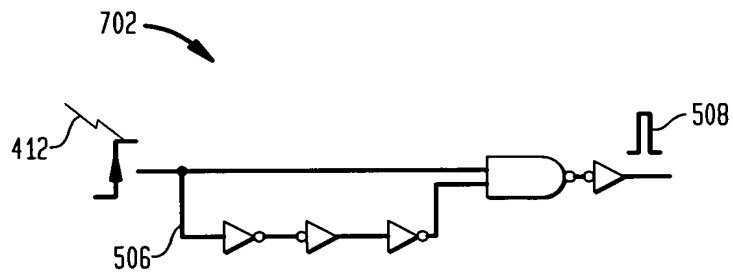
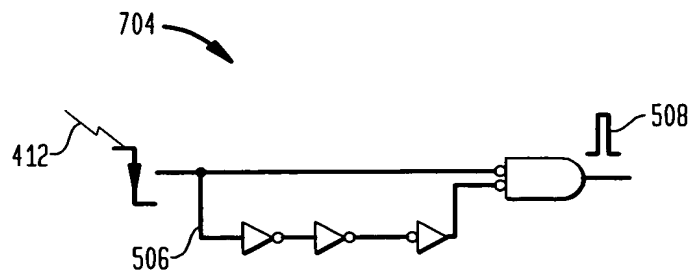
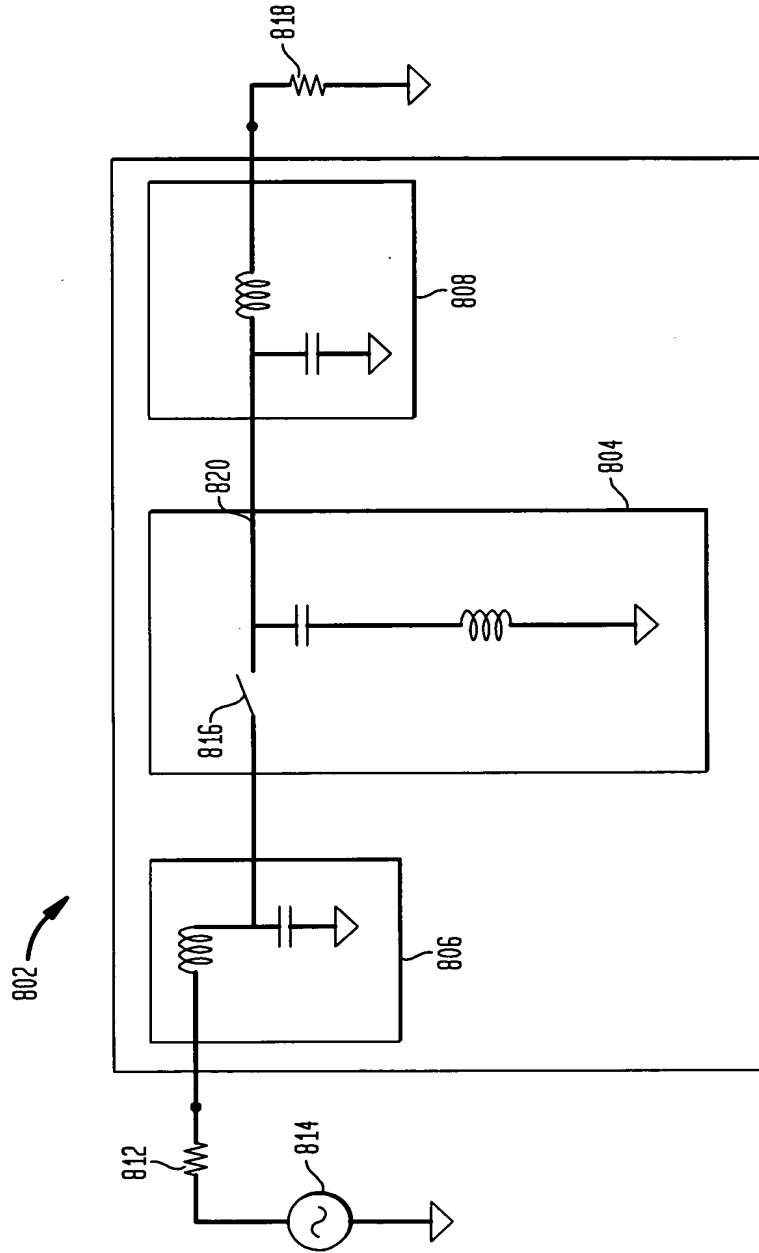


FIG. 7B
FALLING-EDGE PULSE GENERATOR



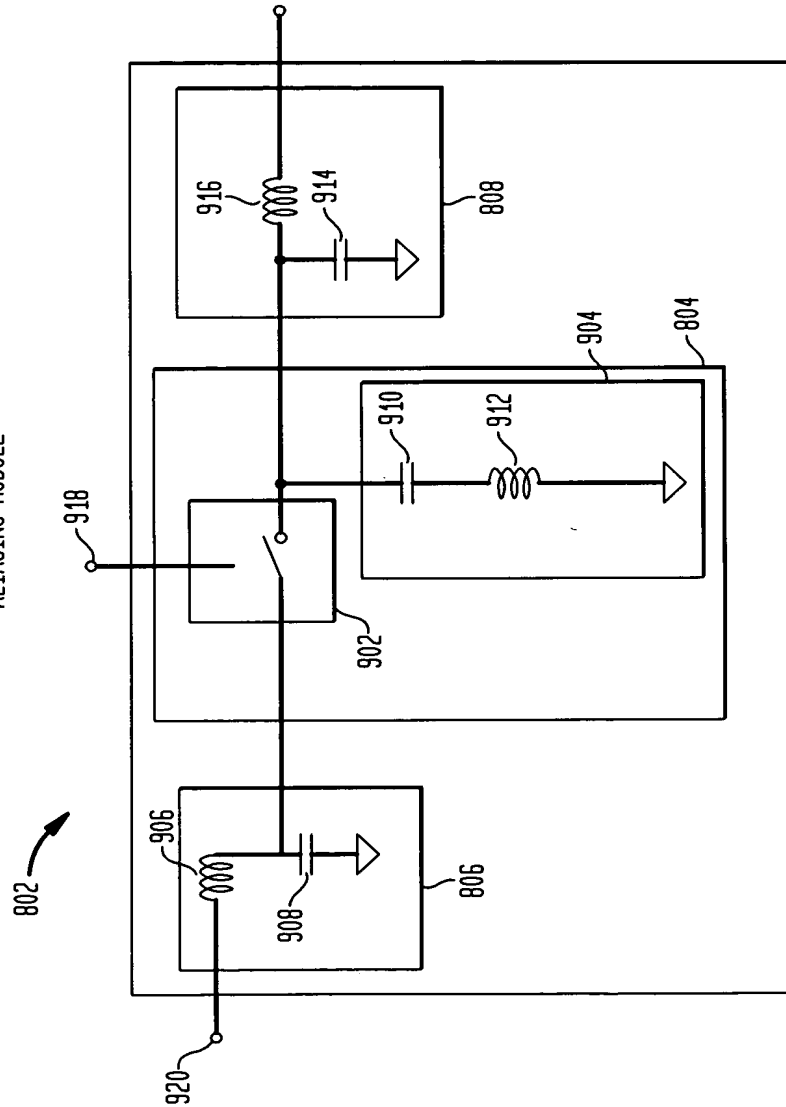
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FIG. 8
 IMPEDENCE MATCHED ALIASING MODULE



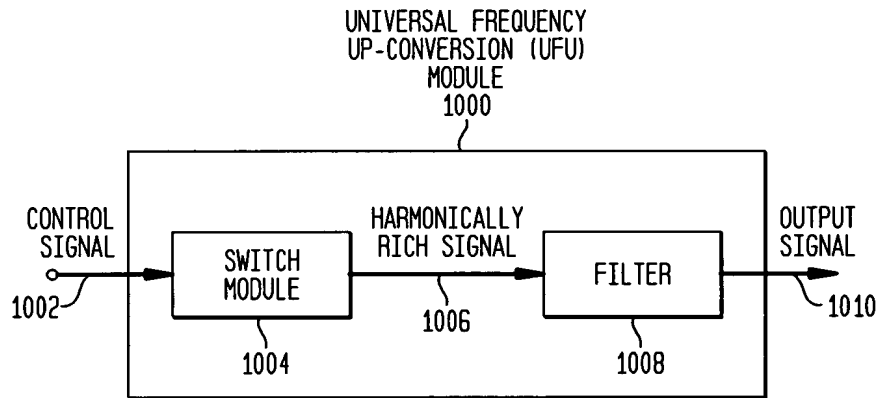
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FIG. 9
ALIASING MODULE



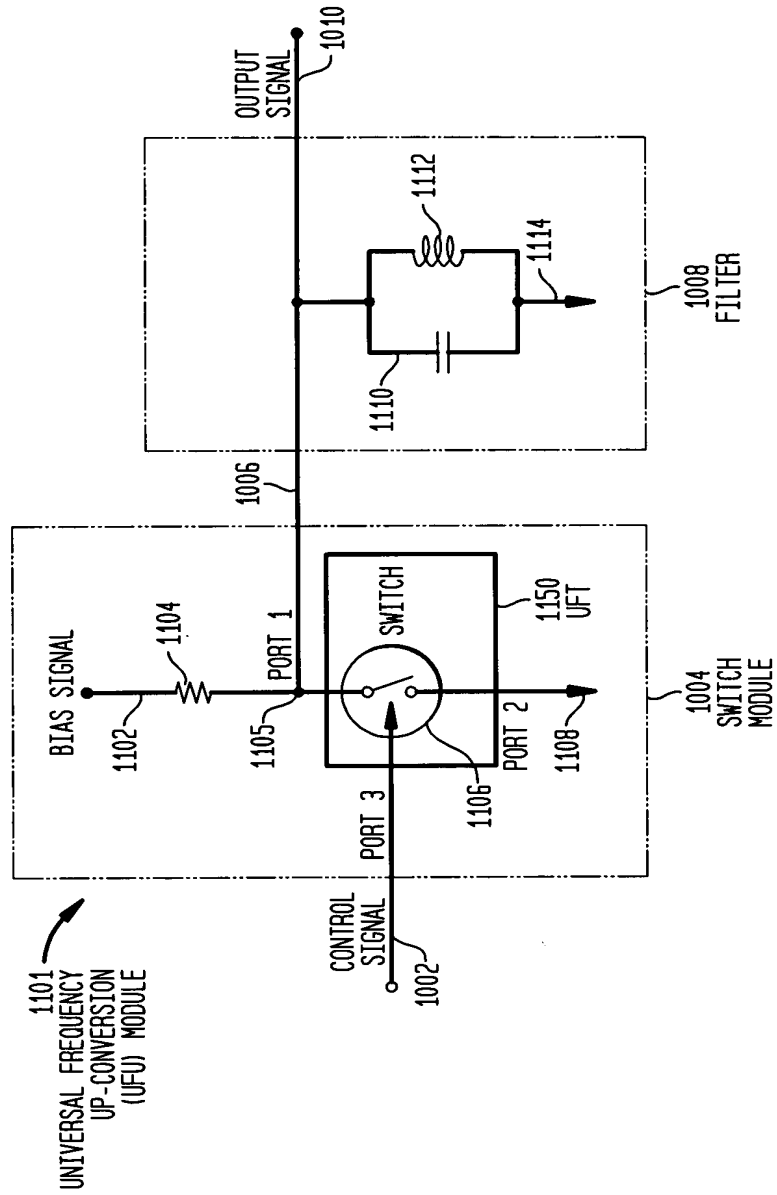
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FIG. 10



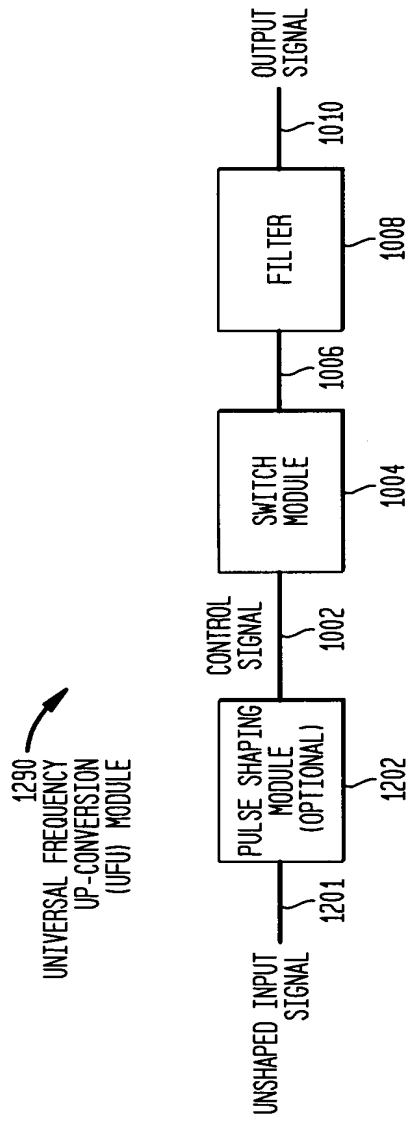
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FIG. 11



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FIG. 12



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FIG. 13A

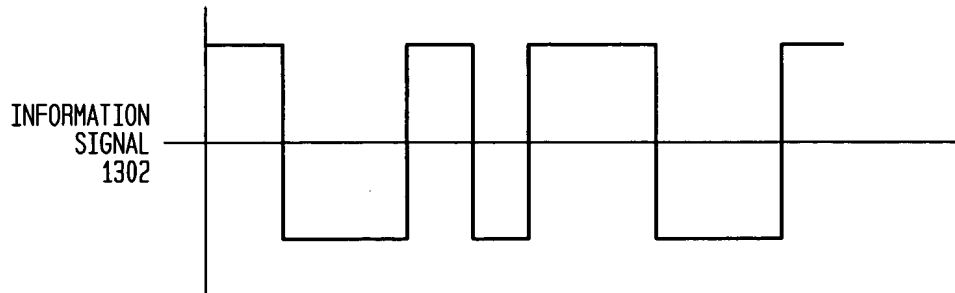


FIG. 13B

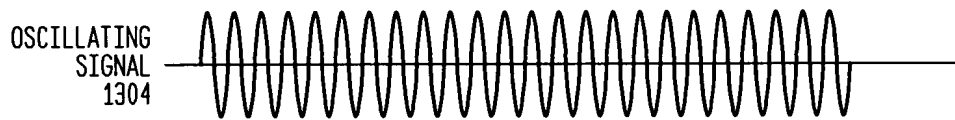


FIG. 13C

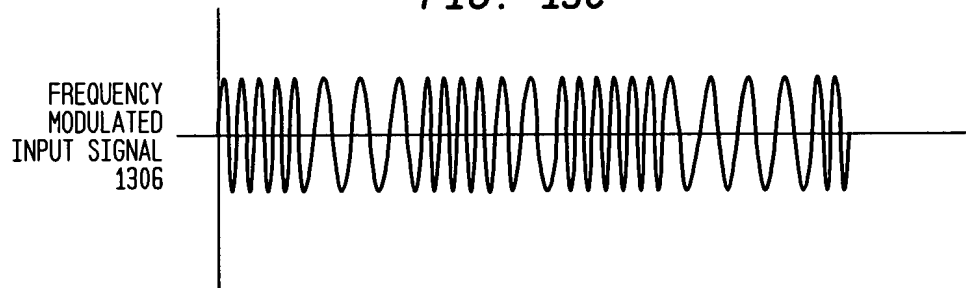
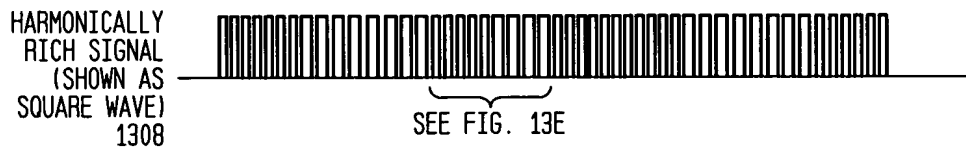


FIG. 13D



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FIG. 13E

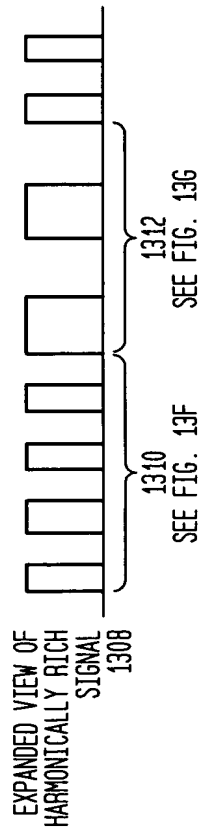


FIG. 13F

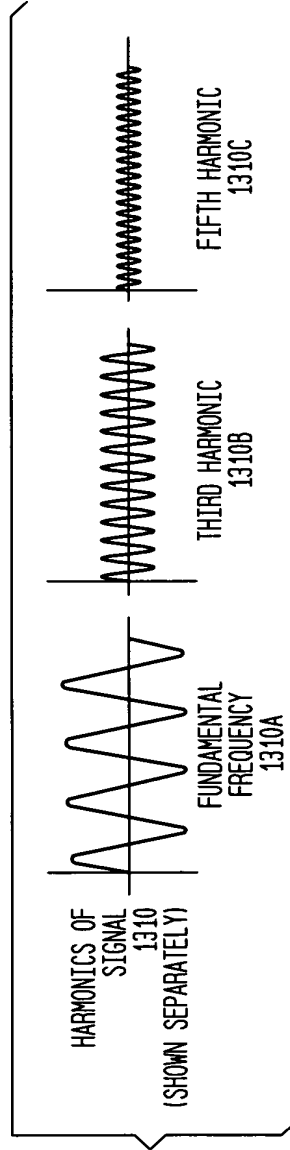
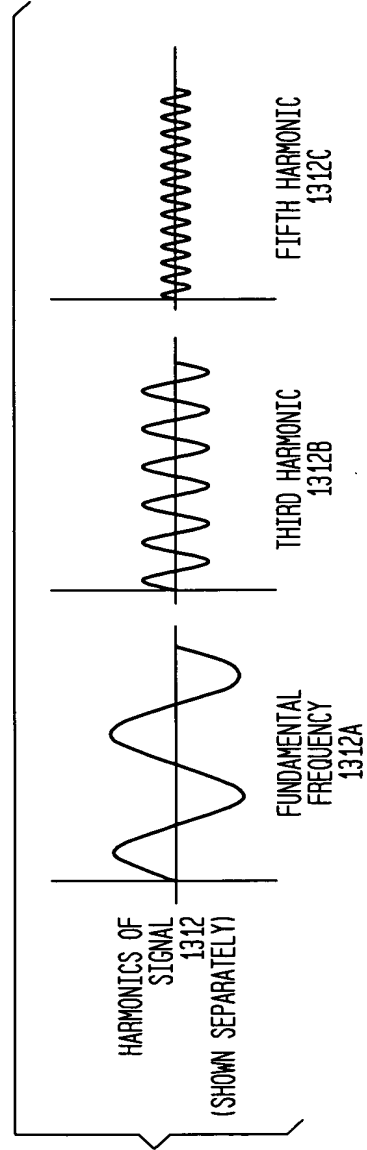


FIG. 13G



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FIG. 13H

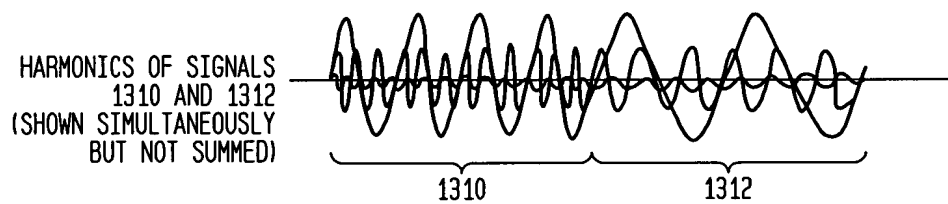
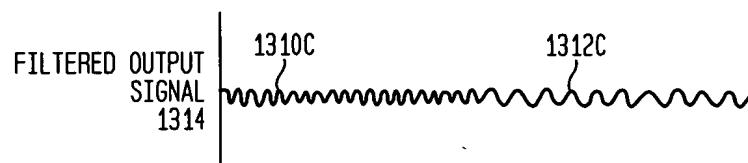
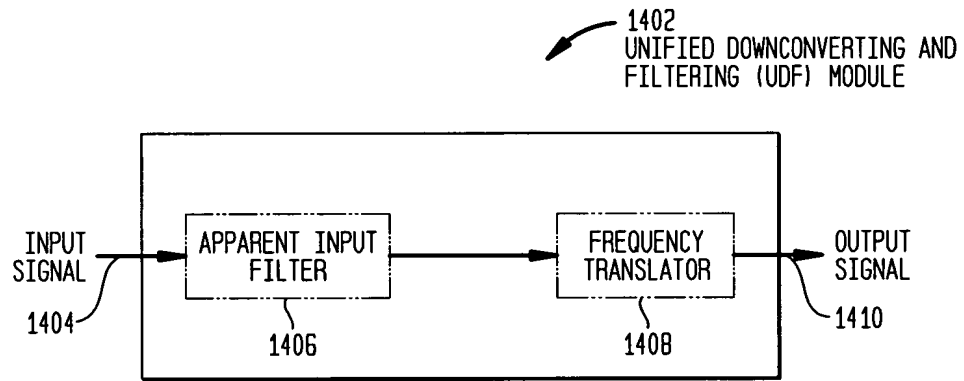


FIG. 13I



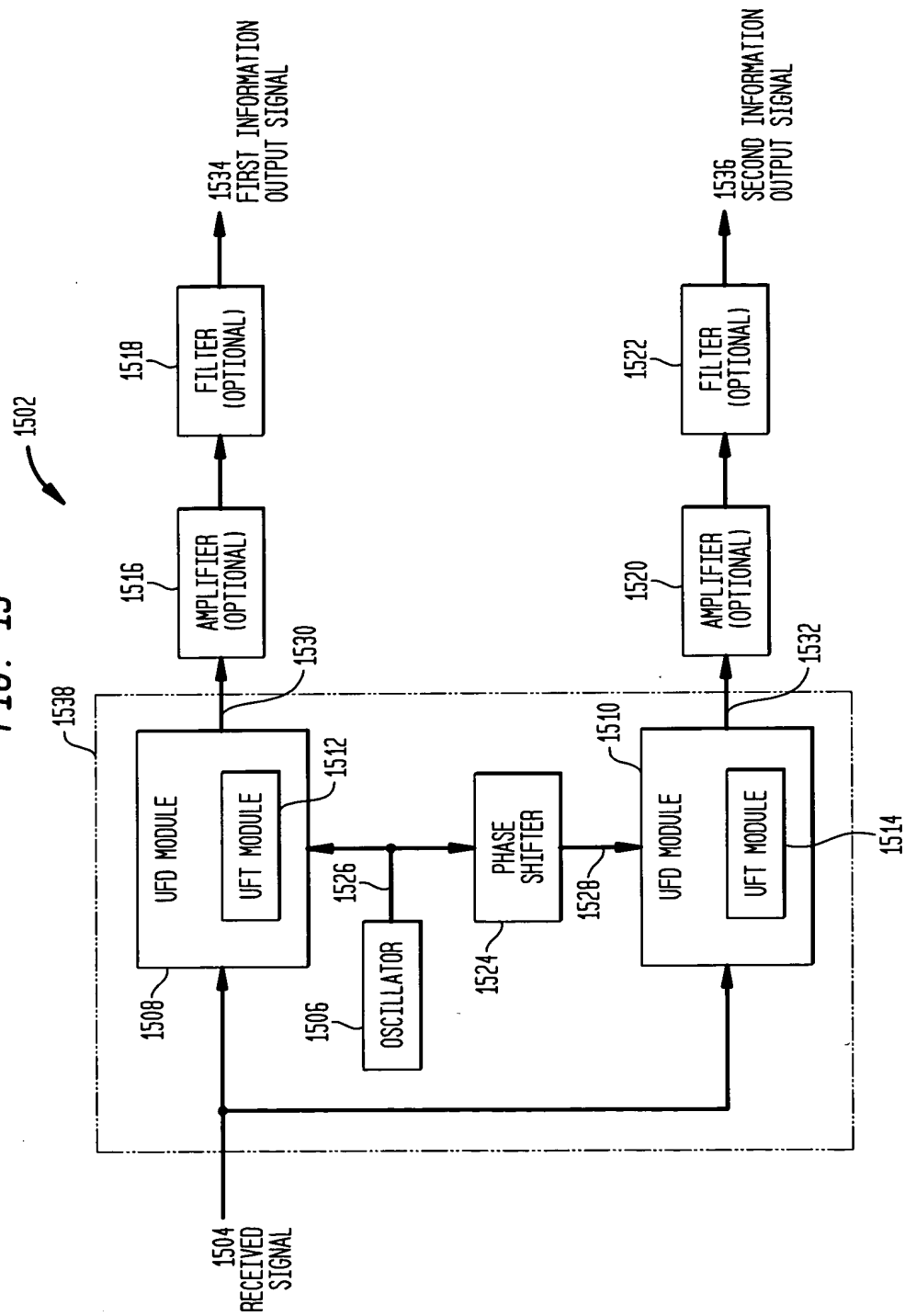
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FIG. 14



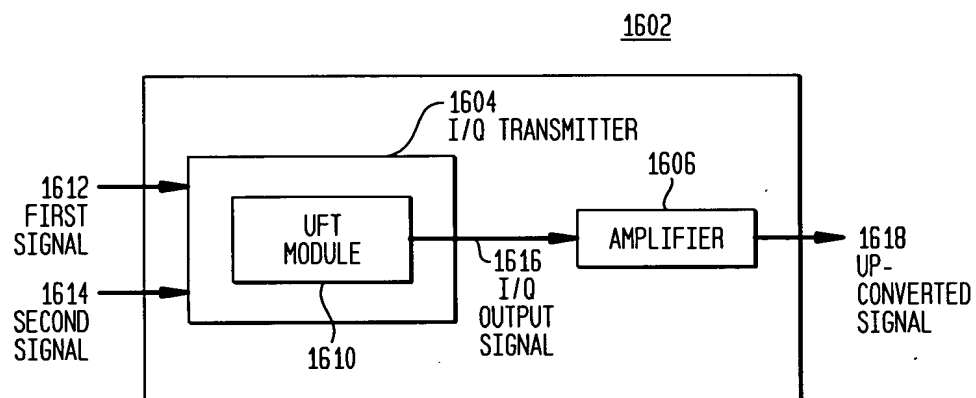
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FIG. 15



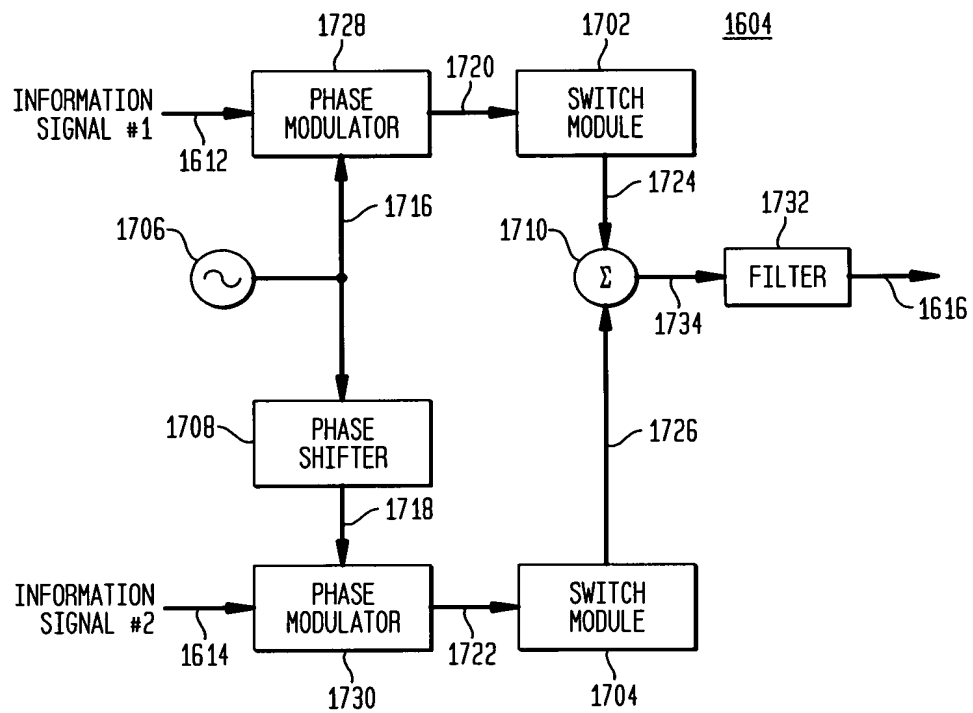
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FIG. 16



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FIG. 17



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FIG. 18

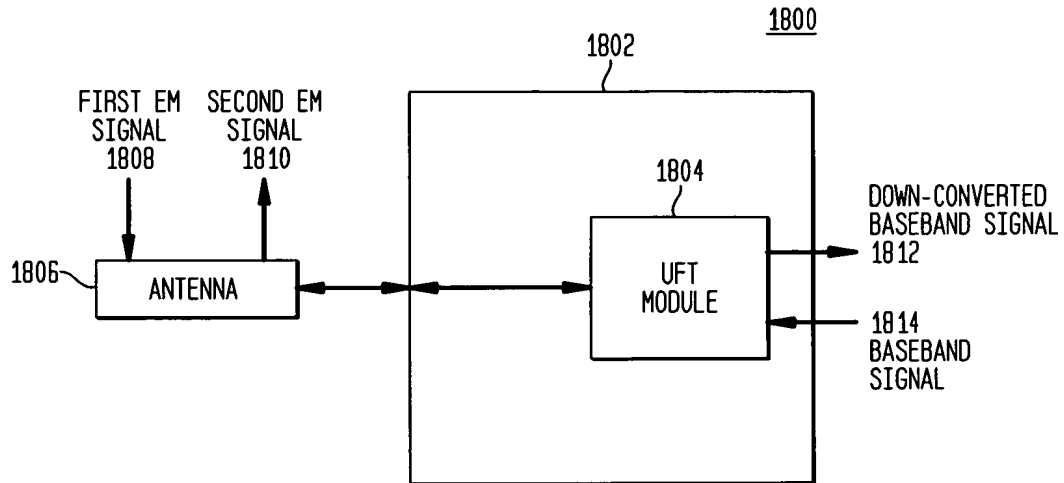
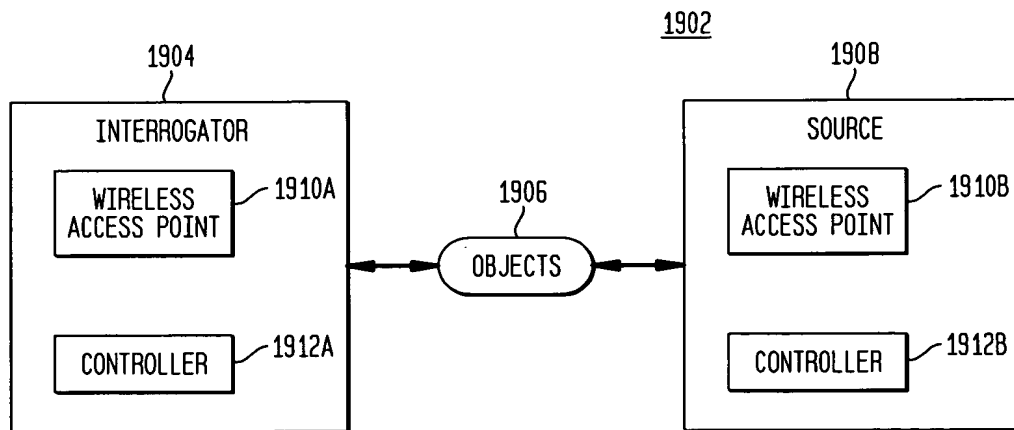
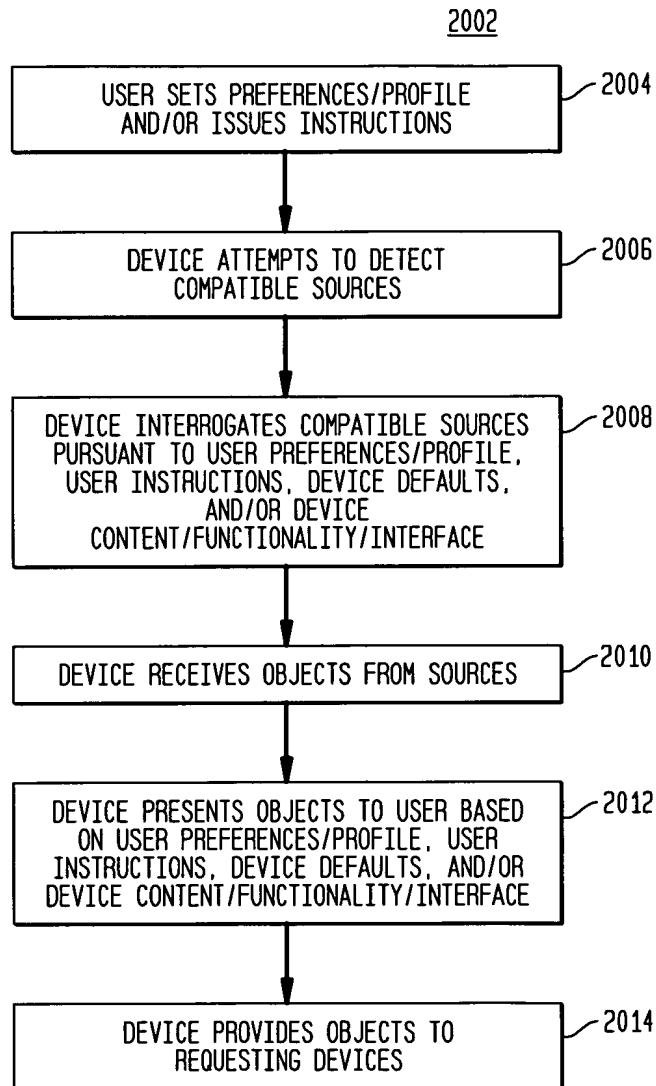


FIG. 19



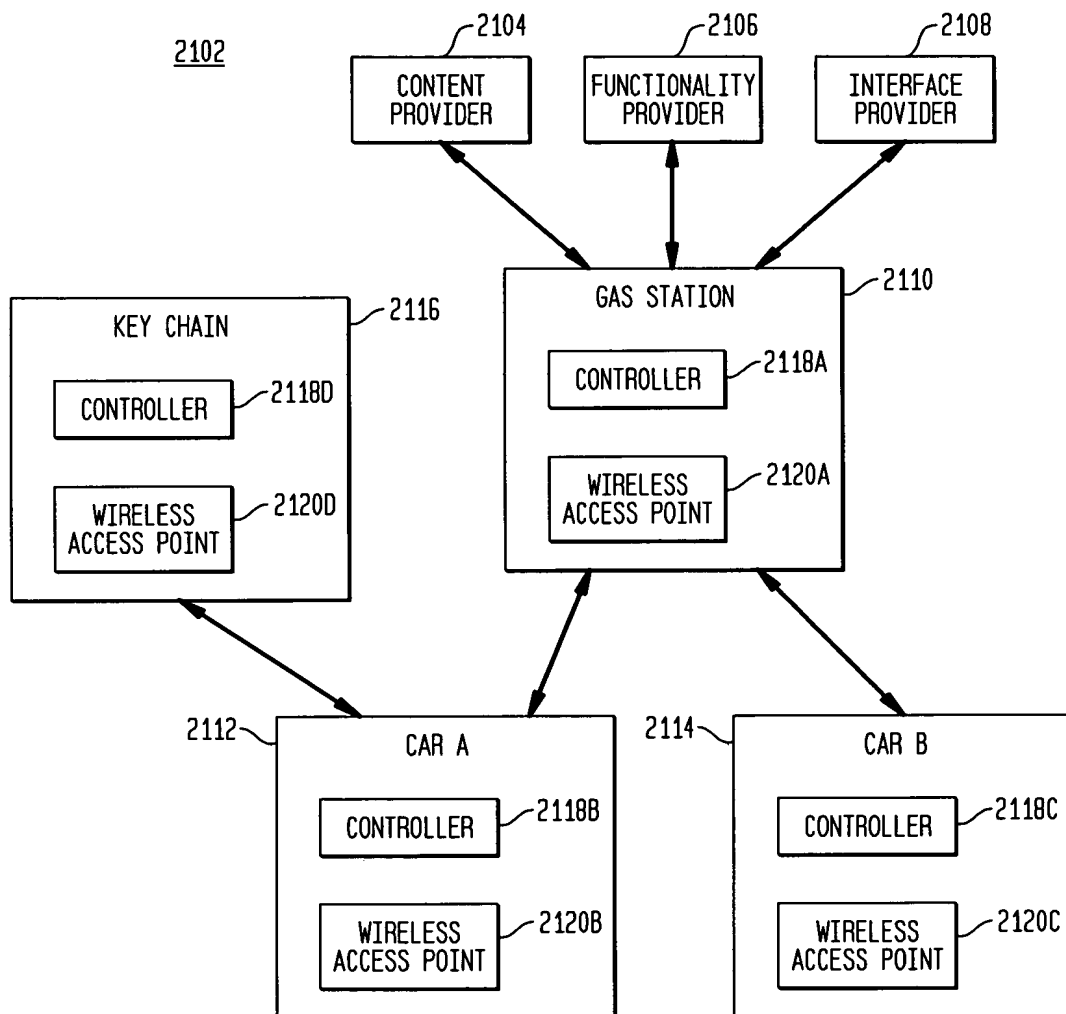
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FIG. 20



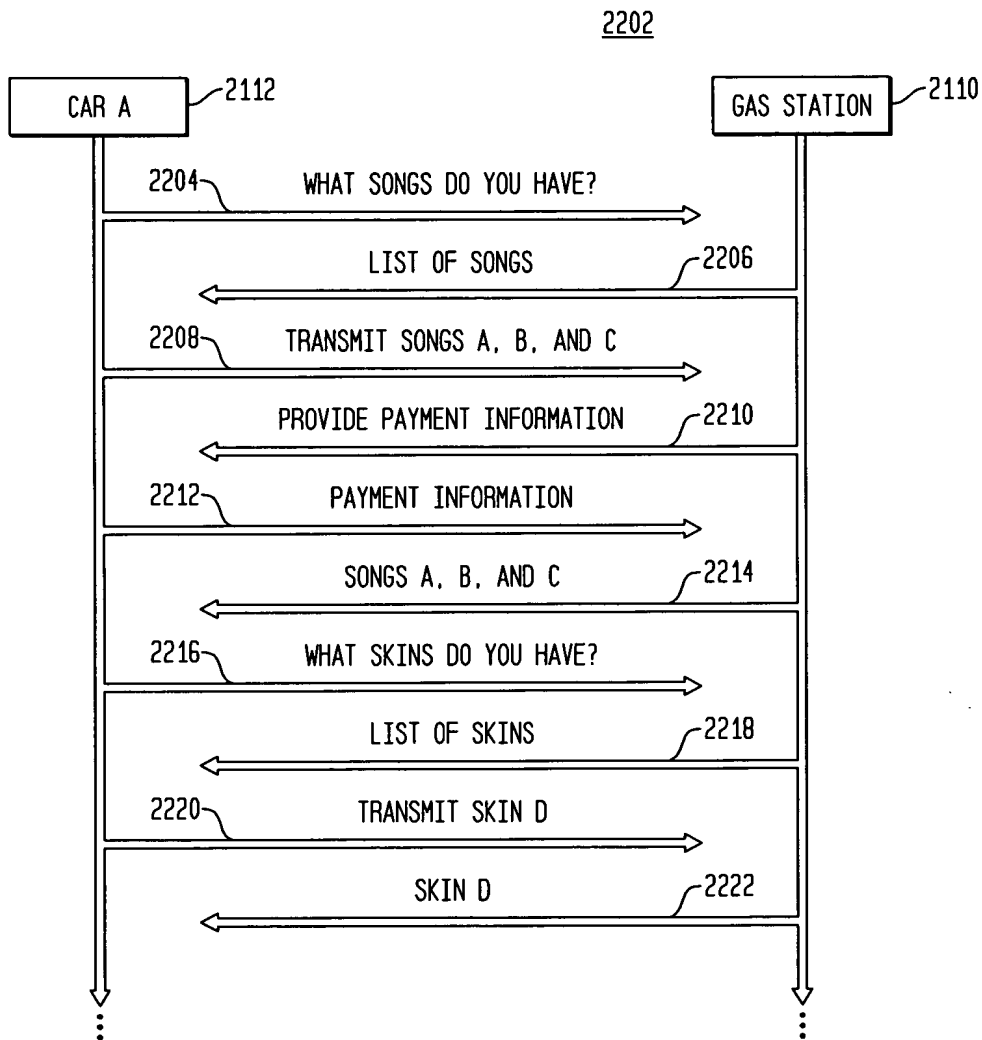
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FIG. 21



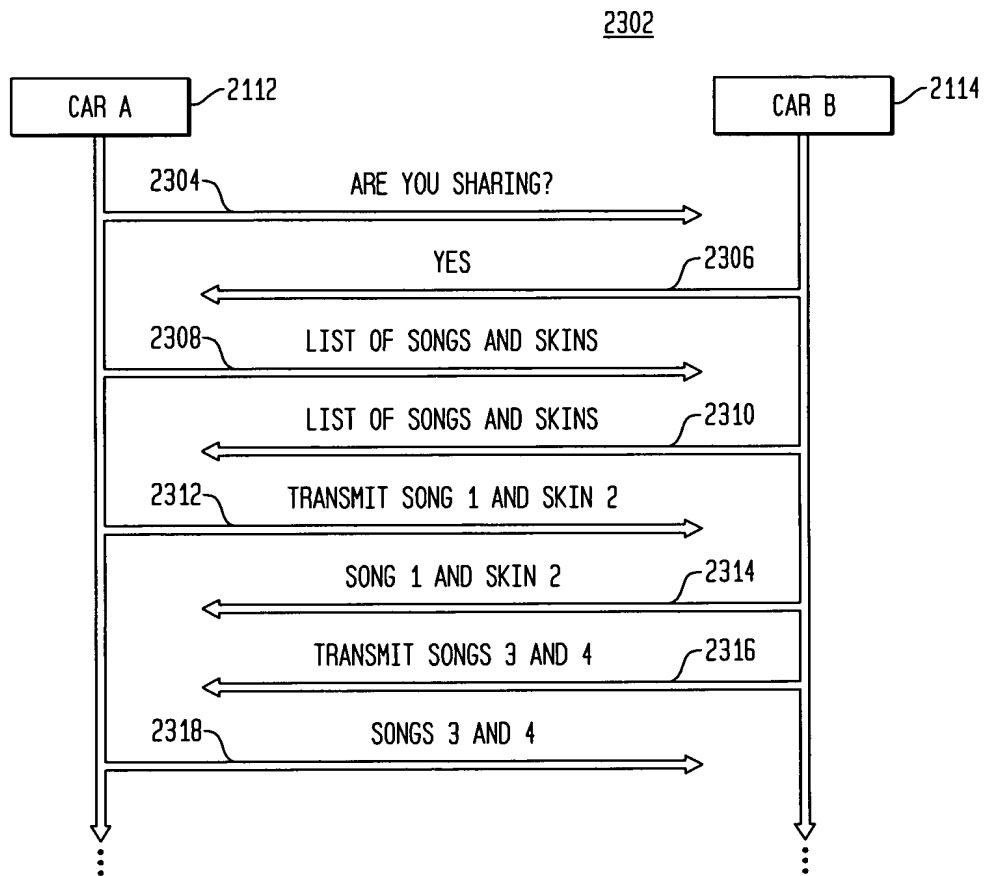
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FIG. 22



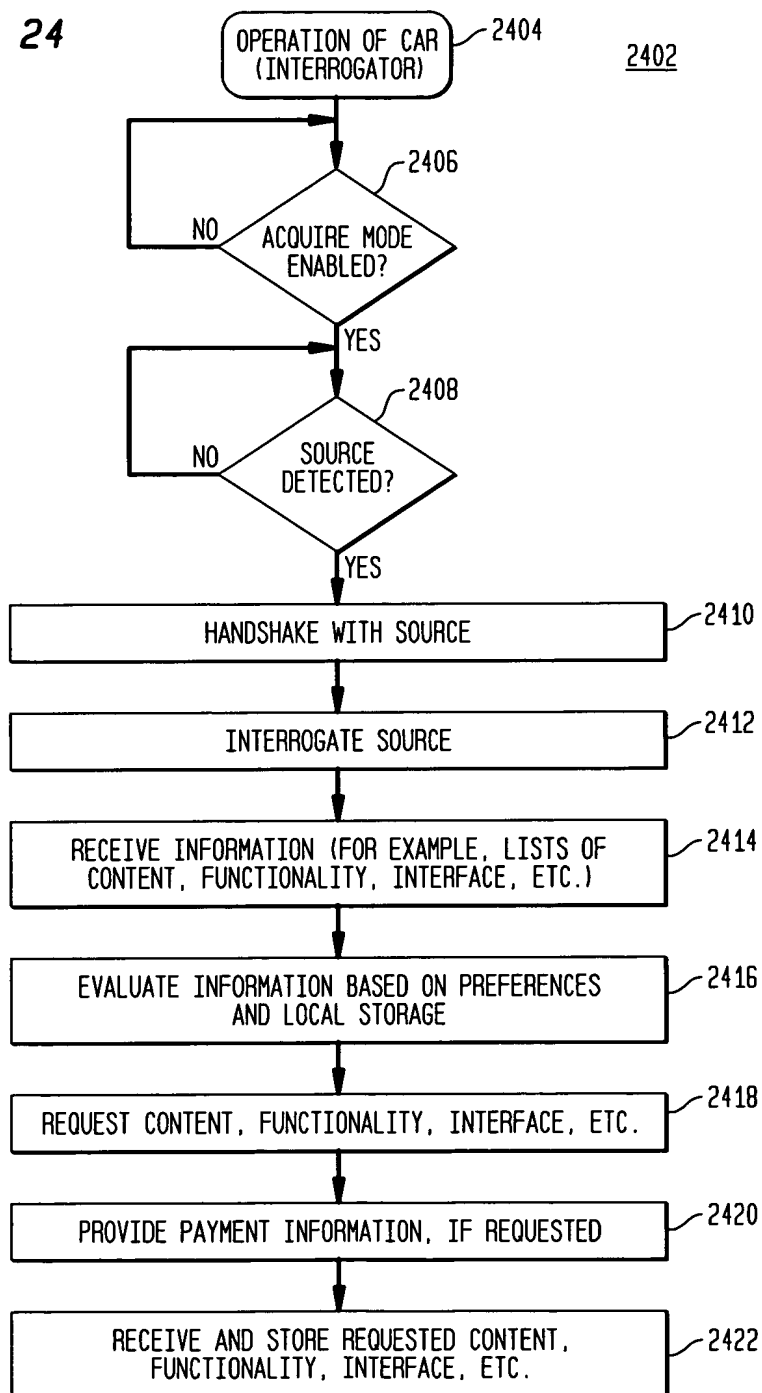
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FIG. 23



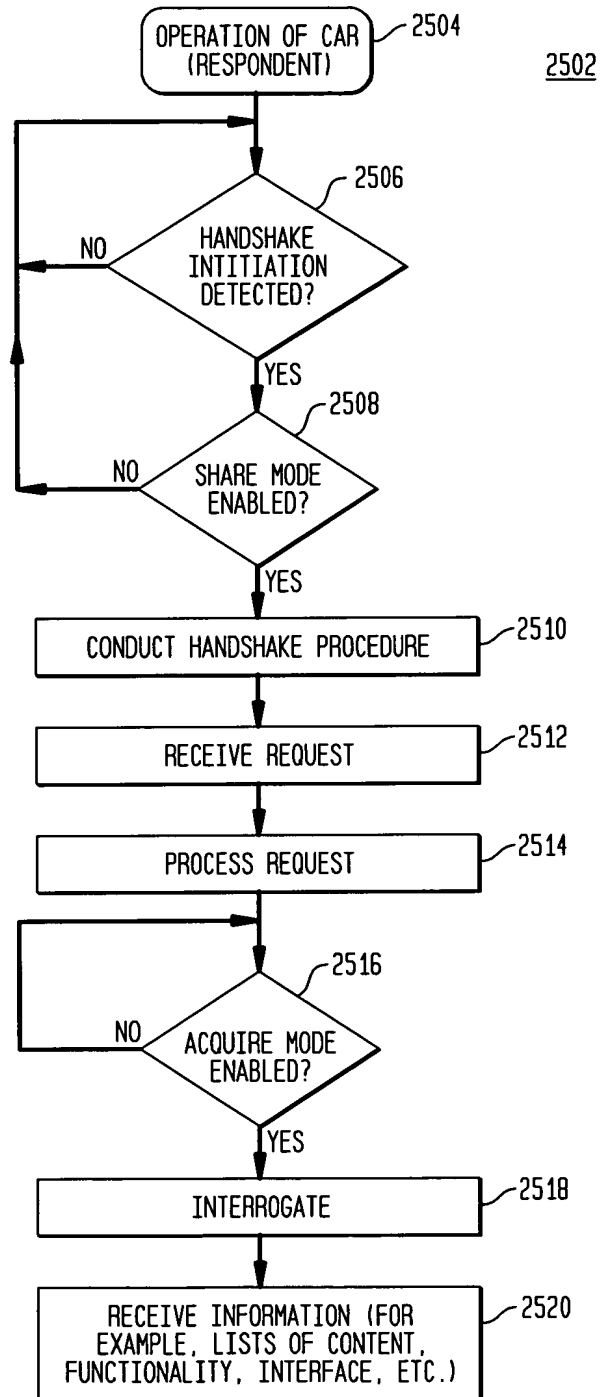
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FIG. 24



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FIG. 25



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FIG. 26

